

MAY 21 2007

Attorney's Docket No.: 13361-069001
Client's Ref. No.: MP0347

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Number of pages including this page 6

Applicant : George Chien et al.
Serial No. : 10/825,781
Filed : April 16, 2004

Art Unit : 2817
Examiner : Henry Choe


Title : AMPLIFIER CALIBRATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

A Response to Notice of Allowance, completed issue fee transmittal form PTOL-85b,
Comments on Examiner's Reasons for Allowance and a Submission of Credit Card payment for
\$1400 for the required fee dated 5/21/07 is attached.

Date: 5/21/07

Respectfully submitted,


Mark D. Kirkland
Reg. No. 40,048

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MAY 21 2007

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Serial No. : 10/825,781

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Art Unit : 2817

Examiner : Henry Choe

Confirmation No.: 9068

Notice of Allowance Date: 2/21/2007

MAIL STOP ISSUE FEE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

RESPONSE TO NOTICE OF ALLOWANCE

In response to the Notice of Allowance mailed February 21, 2007, enclosed are a completed issue fee transmittal form PTOL-85b, Comments on Examiner's Reasons for Allowance and a Submission of Credit Card payment for \$1400 for the required fee.

Please apply any additional charges or credits to our Deposit Account No. 06-1050.

Respectfully submitted,

Date:

5/21/07

M. Kirkland

Mark D. Kirkland

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Suzanne Forno

Typed or Printed Name of Person Signing Certificate

Suzanne Forno

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COMMENTS ON EXAMINER'S REASONS FOR ALLOWANCE

Applicants recognize that in accordance with M.P.E.P. § 1302.14, the Examiner's reasons for allowance need not set forth all of the details as to why the claims are allowed. In the above-referenced application, Applicants do not concede that the Examiner's stated reasons for allowance are the only reasons for which the claims are allowable. In particular, Applicants do not concede that all of the identified limitations—i.e., "Regarding claims 1, 9, 17, 25 and 33, the closest prior art of record, Itoh (Fig. 3) does not disclose the following limitations: the amplifier being operable to reduce variation of the DC offset of the output signal by a factor that corresponds to a gain of the amplifier. Regarding claims 2, 10, 18, 26 and 34, the closest prior art of record, Itoh (Fig. 3) does not disclose the following limitations: the functional limitations of the correction circuit. Regarding claims 7, 15, 23, 31 and 39, the closest prior art of record, Itoh (Fig. 3) does not disclose the following limitations: the amplifier is a unity gain buffer amplifier. Regarding claims 8, 16, 24, 32 and 40, the closest prior art of record, Itoh (Fig. 3) does not disclose the following limitations: the circuit is compliant with one or more of the IEEE standards."—are included in each identified claim or necessary to distinguish over the prior art of record or to satisfy the requirements of 35 U.S.C. § 112. Furthermore, the claims may be patentable for other reasons. In addition, dependent claims 3-6, 11-14, 18-22, 27-30, 35-38 are

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Page : 2 of 2


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allowable on their own merits, and are allowable on the basis of a sub-combination of the recited features of the dependent claims and their respective base claims.

Please apply any additional charges or credits to our Deposit Account No. 06-1050.

Respectfully submitted,

Date: 5/21/07


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